

Date: September 6, 2001

Group Art Unit: 2185

Examiner:



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

M. Ueda

Serial No.: 09/682,188

Filed: August 2, 2001

Docket No.: JP920000046US1

For: Multiprocessor System, Proces fule For Use Therein, And Task

SEP 1 0 2001

Allocation Method 1: Multimrocessing

Assistant Commissioner for Patents

Washington, D.C. 20231

TRANSMITTAL LETTER

Sir:

Please find enclosed a listing (PTO Form 1449) of references which may be relevant to the above-referenced application. This listing of references is not a representation that a search has been made, that better references than listed do not exist, or that ether references are not applicable. Moreover, this listing does not constitute any admission by applicants or applicants' representative that the information provided derein is necessarily prior art to applicants' invention. Please charge any fee necessary to enter this paper to deposit account 09-0468.

Respectfully submitted,

Derek S. Jennings Reg. No. 41,473 (914) 945-2144

IBM Corporation Intellectual Property Law Dept. P.O. Box 218 Yorktown Heights, New York 10598

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				FILING DATE: August 2, 2001	<u> </u>			-	
REFEREN	ICE	DESIGNATION	U.S. PA	TENT DOCUMENTS		,	-		
EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING (IF AF	DATE PPRO.)	
	AA	5,713,004	1/27/98	Kimmel et al.	G06F	12/08			
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	AH	JPUP4-321136	11/11/92	JP	G06F	9/46			
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